IN THE CLAIMS:

Kindly cancel claim 18 without prejudice and amend the claims as follows:

14. (Amended) An electronic imaging system comprising:

a solid-state image sensor having a two-dimensional array of pixels capable of converting light incident thereon to electric signal, the pixels being arranged in a plurality of horizontal lines, the lines being arranged vertically one under another;

a color filter arranged on a incident plane of the solid-state image sensor having a line sequential mosaic pattern; and

control means for selectively controlling a mode for sequential scan reading out pixel signals concerning the whole pixels of the solid-state image sensor for still picture recording, and a mode for reading out pixel signal sums by utilizing a plurality of vertical registers each of n ($n \geq 2$, n being an integer) lines among m ($m \geq 3$, m being an integer) lines in k ($k \geq 6$, k being an integer) continuous lines of the solid-state image sensor for still picture recording or dynamic image processing.

15. (Amended) An electronic imaging system comprising:

a solid-state image sensor having a two-dimensional array of pixels capable of converting light incident thereon to electric signal, the pixels being arranged in a plurality of horizontal lines, the lines being arranged vertically one under another ;

a color filter arranged on a incident plane of the solid-state image sensor having a line sequential mosaic pattern ; and

control means for selectively controlling a mode for sequential scan reading out pixel signals concerning the whole pixels of the solid-state image sensor for still picture recording,

a mode for reading out pixel signal sums by utilizing a plurality of vertical registers each of n ($n \geq 2$, n being an integer) lines among m ($m \geq 3$, m being an integer) lines of the solid-state image sensor for still picture recording or dynamic image processing, and a mode for reading out pixel signal sums by utilizing a plurality of vertical registers of n lines among m lines in k ($k \geq 6$, k being an integer) partially continuous lines of the solid-state image sensor for still picture recording or dynamic image processing.

16. (Amended) The electronic imaging system as set forth in claim 14 or 15, in which the control means controls a mode of reading a plurality of k line blocks each of k lines in the whole lines for said still picture recording or dynamic image processing.

17. (Amended) The electronic imaging system as set forth in claim 14, 15 or 16 in which image data obtained by reading out said pixel signal sums each of n lines among m vertically continuous lines for still picture recording or dynamic image processing, is such that its color signal is line sequential data.

22. (Amended) The electronic imaging system as set forth in claim 14, 15 or 16, in which dynamic image processed signal obtained in either of the modes is used for AF, AE or AWB control data.

23. (Amended) The electronic imaging system as set forth in claim 14, 15 or 16, in which dynamic image processed signal obtained in either of the modes is used as AF, AE or AWB control data, and the AF, AE or AWB control data is calculated sequentially each in each frame.

24. (Amended) The electronic imaging system as set forth in claim 15, in which the control means selects a mode of reading out pixel signal sums each of n lines among m vertically continuous lines when obtaining dynamic image processed signal to be displayed on a display provided in, the system to be supplied to an external display provided outside the system or to be used as AE or AWB control data, and the control means selects a mode of reading out pixel signals of n lines among every m vertically continuous lines in k partially continuous lines when obtaining dynamic image processed signal to be used as AF or AE control data.

See attached Appendix for the changes made to effect the above specification.

Please add new claim 38 as follows:

38. (New) An electronic imaging system comprising:

a solid-state image sensor having a two-dimensional array of pixels capable of converting light incident thereon to electrical signals, the pixels being arranged in a plurality of horizontal lines, the lines being arranged vertically one under another ;

a color filter arranged on a incident plane of the solid-state image sensor having a line sequential mosaic pattern; and

control means for selectively controlling a mode for sequential scan reading out pixel signal concerning the whole pixels of the solid-state image sensor for still picture recording and a mode for reading out pixel signal sums of n lines out of every m lines within partially continuous k lines of the solid-state image sensor by utilizing a plurality or vertical registers for still picture recording or dynamic image processing, wherein

$n \geq 2$, n being an even number,

$m \geq n+1$, m being an odd number,

$k \geq 2m$, k being an integer.